

Fig. 1

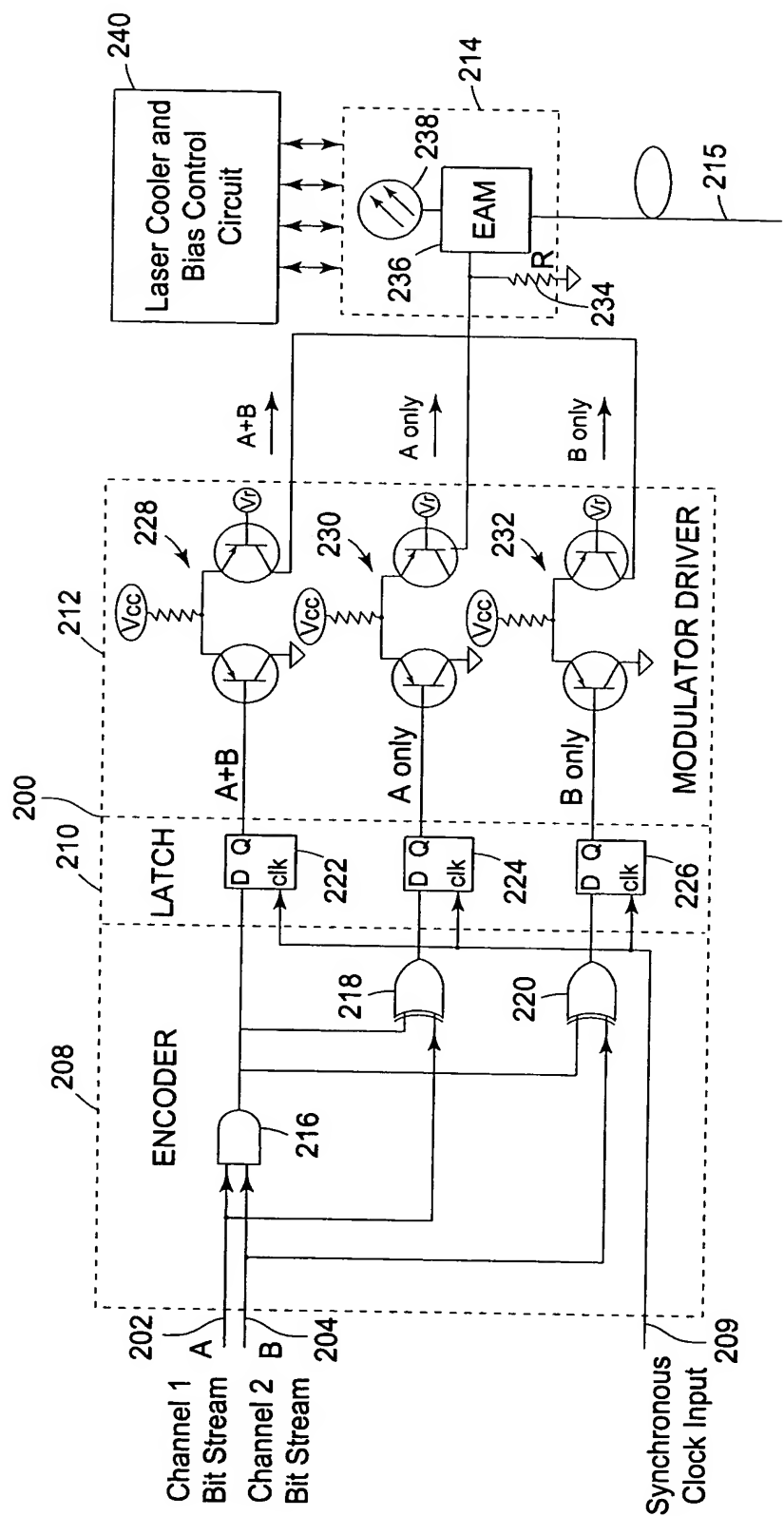
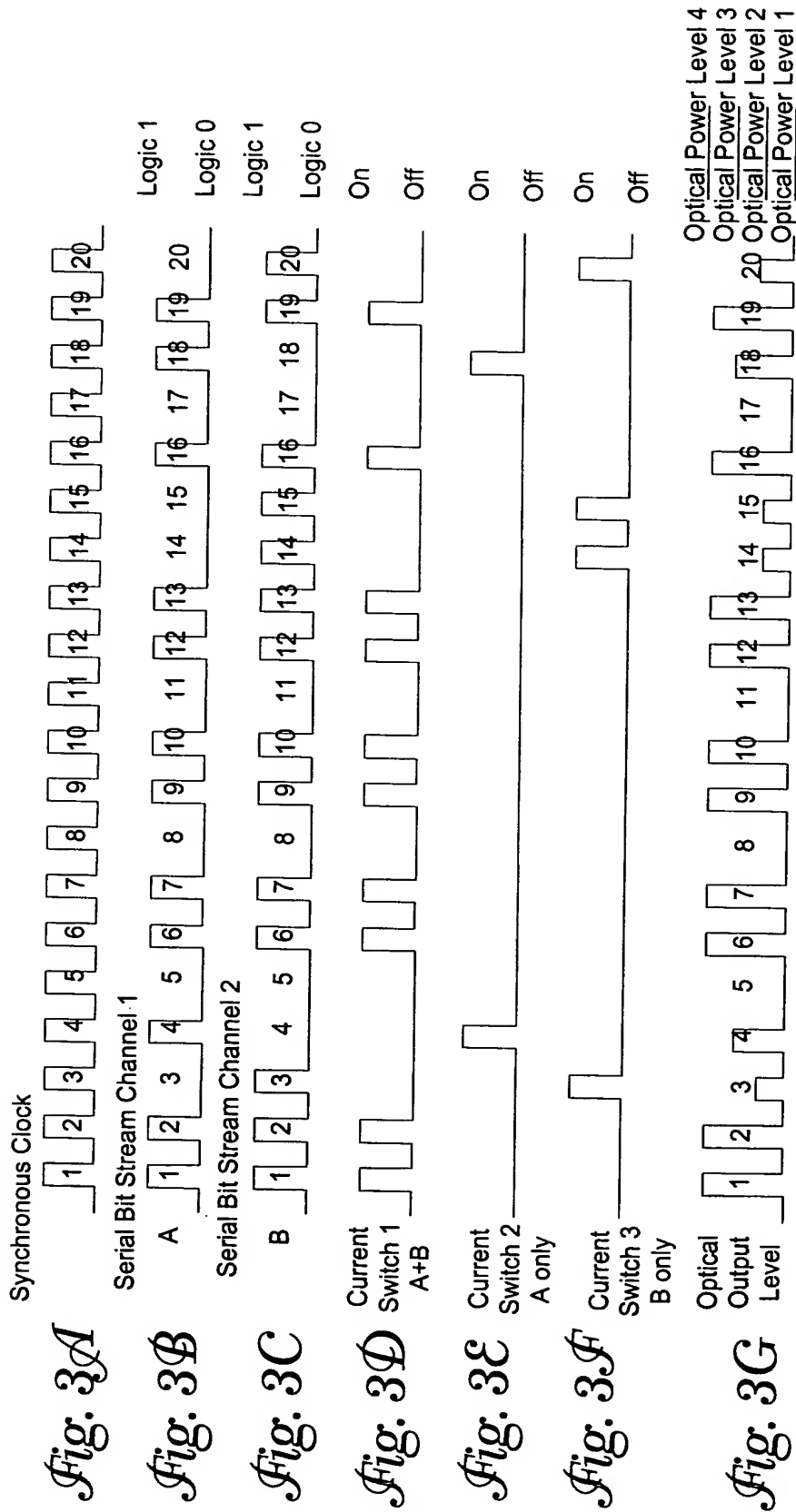
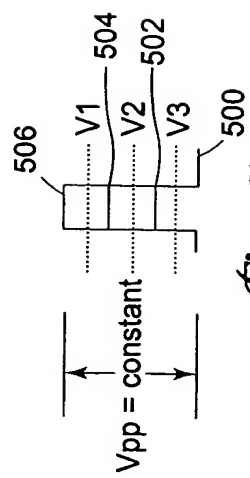
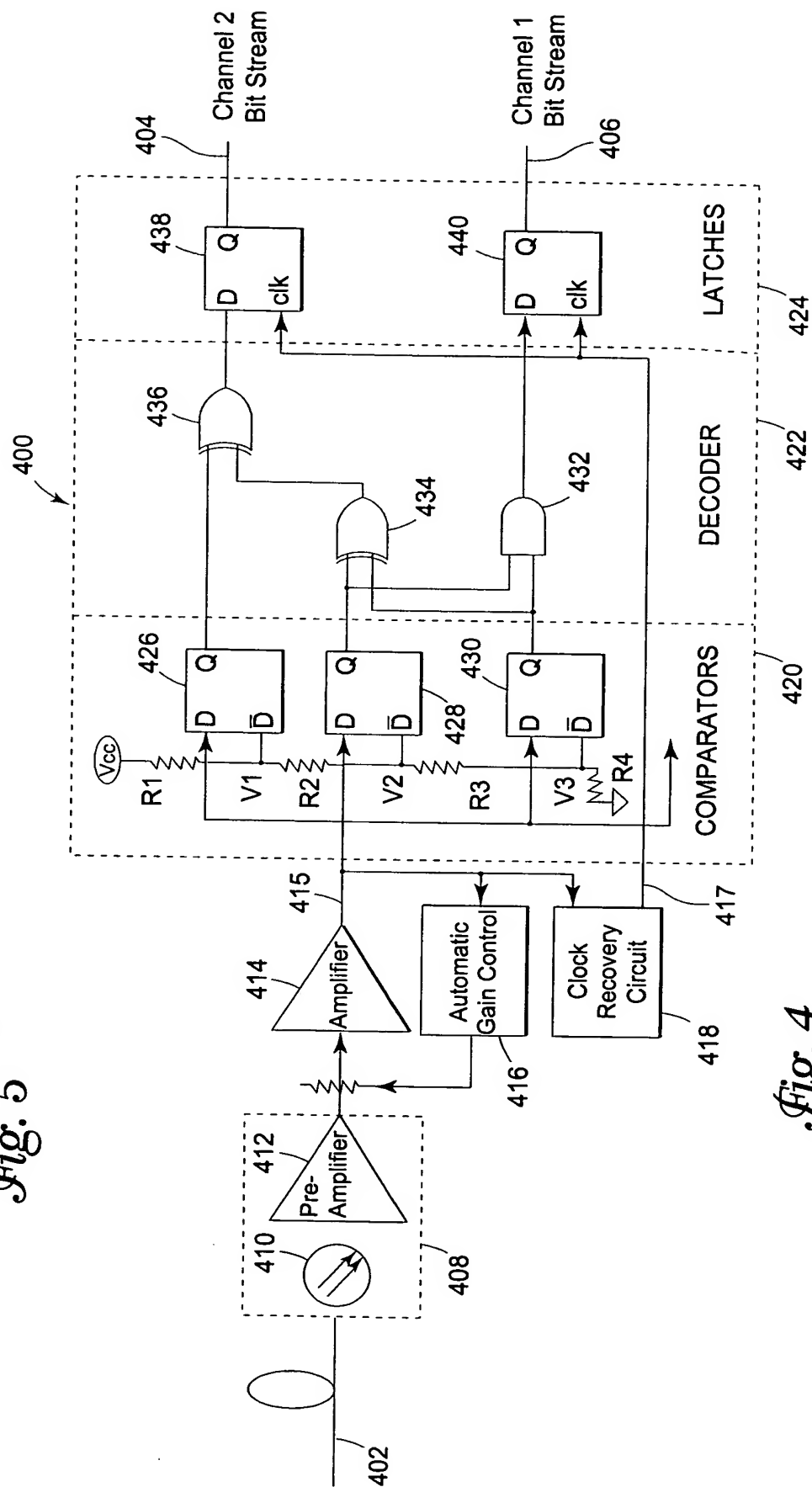


Fig. 2

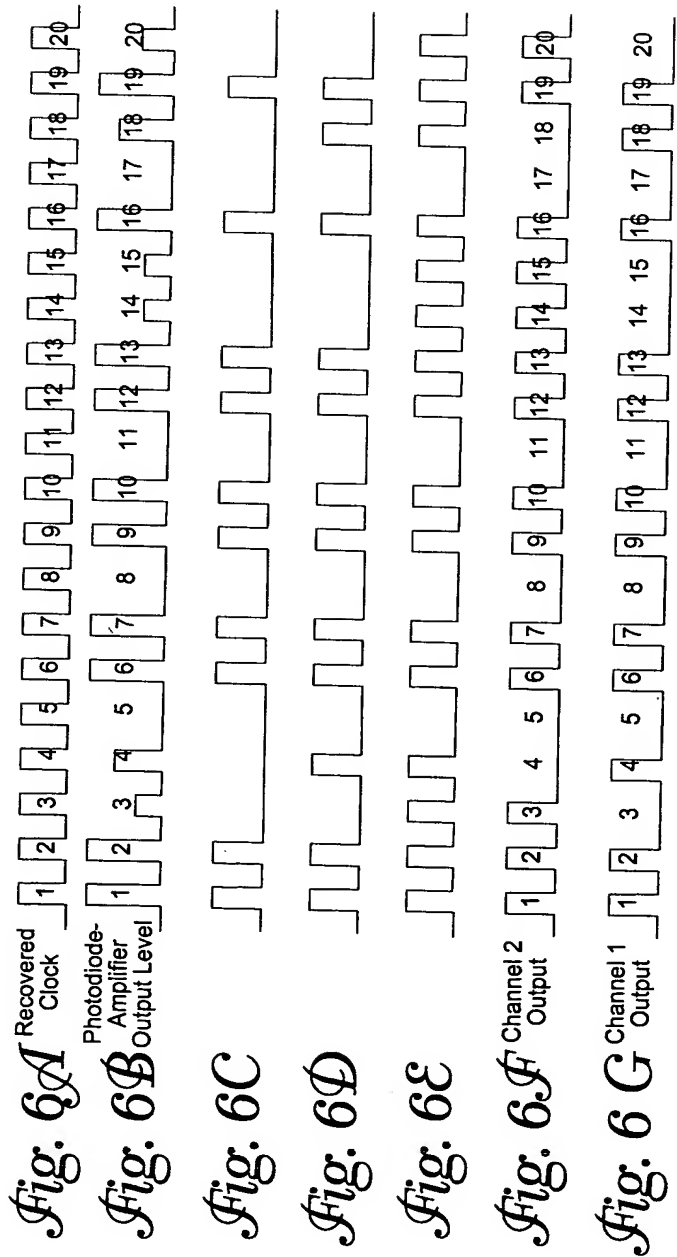




*Fig. 5*



*Fig. 4*



Optical Input Power Level 4 - Output Bit on Channel 1 and 2  
 Optical Input Power Level 3 - Output Bit on Channel 1 only  
 Optical Input Power Level 2 - Output Bit on Channel 2 only  
 Optical Input Power Level 1 - No Bits are output